GBGS Schama

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ı		Fourt	h Sei	meste	er B.E	. Deg	ree Exa	eminatio:	n. June	July 2018	,
						_		nd Me	-	-	
ne	: 3	hrs.					1999			Max. M	arks: 80
	No	te: Answ	er any	FIVE	full qu	estions	s, choosin	g one full q	uestion fr	om each mo	dule.
					- A	N I	Module-1	<u>L</u>			
).							limiting erro e ammeters		help of a 1	(06 Marks) neat circuit (10 Marks)
_).		note oi	n mult	i range	voltme		of limitatior explain how		e of a voltme	(10 Marks) eter can be (06 Marks)
a Į		Explain i Discuss v				techni	nd DAC.		ntages and	d limitations.	(10 Marks) (06 Marks)
	a. o.						OR integration inciples of	g type DVN ADC.	1 .	(6) (6)	(08 Marks) (08 Marks)
	a. b.					scuss l	itor with th	3 ram of oscil ne help of a			(08 Marks) (08 Marks)
,	a. b.	Explain :					OR scope.				(08 Marks (08 Marks
	a. b.	Explain :	about t about \	he fiel Whetst	d strengt one's br	h mete	Module- er with the ith a neat	help of the circuit diagr	block diag am.	gram.	(10 Marks (06 Marks
	a. b.	Discuss What is	about r Megge	neter. r? Exp	State its	advant es of m	OR tages. negger and	working of	megger.		(08 Marks (08 Marks
	a.	With the	,	of nea	diagrar		Module- lain the w		VDT. Me	ention its adv	antages and

(06 Marks)

Write a note on electrical transducers.

OR

10 a. Explain in detail about inductive transducer and its types.
b. Write a note on capacity transducer. (10 Marks) (06 Marks)